Electronic A	Acknowledgement Receipt
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Application Number:	10647247
International Application Number:	
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Title of Invention:	METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE, AND METHOD OF FORMING RESIST PATTERN
First Named Inventor/Applicant Name:	Hajime Yamamoto
Customer Number:	38834
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Attorney Docket Number:	031029
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /₊zip	Pages (if appl.)
1		031029_Amendment_1111_06 -09-2011.pdf	127903 506d576d873d20755046475c46f10e3bf6df 8727	yes	11

	Multipart Description/PDF files in .zip description							
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	Amendment/Req. Reconside	1						
	Clai	2	9					
	Applicant Arguments/Rema	10	11					
Warnings:								
Information:		1-2-0-4 1 0-0-4-0						
2	Power of Attorney	031029_Submission_POA_06-0 9-2011.pdf	85978	no	2			
	2 Fower of Attorney		b6763a8186042207d2e83026dc76f85c866 274ad					
Warnings:				·				
Information:								
3	Power of Attorney	031029_POA_SB80_FUJITSU_S EMI.pdf	142808	no	2			
	5 Fower of Attorney		0a46ab6e8e57f6826c07135bf81c53571ae8 f506					
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4	Power of Attorney	031029_373- Statement_SB-96_06-09-2011 pdf	79067	no	1			
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Information:								
5	Terminal Disclaimer Filed	031029_Terminal_Disclaimer_0 6-09-2011.pdf	87748	no	1			
			bb53f2eb82f436d85590aadb77912294fc28 9406					
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